

AMENDMENTS TO THE CLAIMS:

This listing of claims will replace all prior versions and listings of claims in the application:

1. (Currently Amended) An electronic apparatus having a plurality of operation modes, comprising:

an operation mode setting unit configured to set the operation modes;

a time zone setting unit configured to set a time zone information for carrying out each operation mode of the electronic apparatus;

an operation mode acquisition unit configured to acquire a current operation mode;

an operation mode determination unit configured to independently determine whether or not the current operation mode acquired by the operation mode acquisition unit corresponds to a desired operation mode, ~~based upon~~ by determining whether or not a current time belongs to a time zone of the acquired current operation mode indicated by the time zone information set by the time zone setting unit;

a control unit configured to carry out an operation mode changeover to set the desired operation mode if the operation mode determination unit determines that the current operation mode does not correspond to the desired operation mode; and

an operation mode control unit configured to carry out at least one of screen saver control, monitor power control, hard disk power control, and CPU processing speed control based upon the desired operation mode.

2. (Previously Presented) The apparatus according to claim 1, wherein the operation modes include a first operation mode and a second operation mode, the operation mode control unit turning off a monitor when no operation of the apparatus is made beyond a first time in the first operation mode, and turning off the monitor when no operation to the apparatus is made beyond a second time shorter than the first time in the second operation mode.

3. (Previously Presented) The apparatus according to claim 1, wherein the operation modes include a first operation mode and a second operation mode, the operation mode control unit turning off a hard disk drive when no access is made beyond a first time in the first operation mode, and turning off the hard disk drive when no access is made beyond a second time shorter than the first time in the second operation mode.

4. (Previously Presented) The apparatus according to claim 1, wherein the operation modes include a first operation mode and a second operation mode, the operation mode control unit driving an optical disk drive at a first speed in the first operation mode, and driving the optical disk drive at a second speed lower than the first speed in the second operation mode.

5-12. (Canceled)

13. (Currently Amended) An operation controlling method of an electronic apparatus including a plurality of operation modes, comprising:

setting the operation modes;

setting a time zone information for carrying out each operation mode of the electronic apparatus;

acquiring a current operation mode;

independently determine whether or not the acquired current operation mode corresponds to a desired operation mode, ~~based upon~~ by determining whether or not a current time belongs to a time zone of the acquired current operation mode indicated by the set time zone information;

carrying out an operation mode changeover to set the desired operation mode if the current operation mode does not correspond to the desired operation mode; and

carrying out at least one of screen saver control, monitor power control, hard disk power control, and CPU processing speed control based upon the desired operation mode.

14-16. (Canceled)

17. (Previously Presented) The apparatus according to claim 1, wherein the plurality of operation modes include a normal operation mode and a power save mode; and

only in the power save mode, the screen saver control inhibits start of a screen saver, the monitor power control turns off a monitor after a pre-determined time period since operations of a keyboard and a touch pad stopped, the hard disk control turns off

a hard disk after a pre-determined time period since data access to the hard disk stopped, and the CPU processing speed control reduces a processing speed of a CPU.

18. (Canceled)